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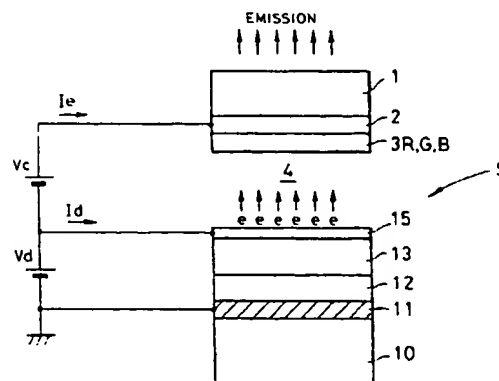
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(54) Electron emission device and display apparatus using the same

(57) An electron emission device comprises an electron-supply layer made of metal or semiconductor and disposed on an ohmic electrode; an insulator layer formed on the electron-supply layer; and a thin-film metal electrode formed on the insulator layer. The electron-supply layer is essentially composed of elements belonging to group IV and contains an additive of at least one material selected from atomic elements belonging to group III or V.

FIG. 1



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## Description

### BACKGROUND OF THE INVENTION

#### 1. Field of the Invention

[0001] The present invention relates generally to an electron emission device, and more particularly to an electron emission display device which has a plurality of electron emission devices arranged in an image display array, for example, in a matrix form.

#### 2. Description of the Related Art

[0002] Conventionally, in field electron emission display apparatuses, a Field Emission Display (FED) is known as a planar emission display device equipped with an array of cold cathode electron emission sources which does not require cathode heating. The emission principle of, for example, an FED using Spindt-type cold cathodes of minute protrusions is as follows. Its emission principle is like a Cathode Ray Tube (CRT), although this FED has a cathode array of Spindt-type protrusions which is different from that of CRT. In the FED, electrons are drawn into a vacuum space by means of each gate electrode spaced apart from the Spindt-type cathode, and the electrons are made to impinge upon the fluorescent substance that is coated on a transparent anode, thereby causing light emission.

[0003] This FED, however, has a problem of low production yield because the manufacture of the minute Spindt-type emitter arrays as a cold cathode is complex and involves many processes.

[0004] There is also known an electron emission device with electron emission devices of metal-insulator-metal (MIM) structure as a planar electron emission source. The electron emission device with the MIM structure comprises an Al underlayer as a base electrode, an  $\text{Al}_2\text{O}_3$  insulator layer with about 10 nm thickness, and a Au overlayer, as a top electrode with about 10 nm thickness which are formed in order on the substrate. In the case that this MIM device is placed under an opposing electrode in a vacuum, when a voltage is applied between the Al underlayer and the Au overlayer and, at the same time, an acceleration voltage is applied to the opposing electrode, then some of electrons emit out of the Au overlayer and reach the opposing electrode.

[0005] However, even the electron emission device with the MIM structure does not yet provide a sufficient amount of emitted electrons.

[0006] To overcome these disadvantages of emission of the MIM device, it is conventionally considered that there is a necessity to make the  $\text{Al}_2\text{O}_3$  insulator layer thinner by about several nanometers and make the  $\text{Al}_2\text{O}_3$  insulator layer with a uniform quality so that the interface between the  $\text{Al}_2\text{O}_3$  insulator layer and the Au overlayer is more uniform.

[0007] To provide a thinner and more uniform insulator layer, for example, an attempt has been made to control the anodized current by using an anodization method thereby to improve the electron emission characteristics, as in the invention described in Japanese Patent Application kokai No. Hei 7-65710.

[0008] However, even an electron emission device with the MIM structure which is manufactured by this anodization method ensures an emission current of about  $1 \times 10^{-5} \text{ A/cm}^2$  and an electron emission efficiency of about  $1 \times 10^{-3}$ .

[0009] Furthermore, for application of the electron emission device to various practical embodiments, it is necessary to seal the electron emission device with a high vacuum by using a heating treatment. Therefore the electron emission device will be subjected to the heating treatment under a high temperature at 300 to 500 centigrade degrees. Such a high temperature during the heating treatment increases the electric resistance of layer in the device to make the electron emission characteristic of the device inferior.

### OBJECT AND SUMMARY OF THE INVENTION

[0010] The present invention has been made in view of the above circumstances, and thus an object thereof is to provide an electron emission device having an electron emitting efficiency high enough to stably emit electrons at a low voltage applied thereto, and moreover a display apparatus including a flat panel display device which employs a plurality of such electron emission devices.

[0011] Moreover another object of the present invention is to provide an electron emission device capable of restricting the increase of the electric resistance of layer caused by the heating treatment.

[0012] The present invention provides an electron emission device which comprises:

- an electron-supply layer made of semiconductor;
- an insulator layer formed on the electron-supply layer; and
- a thin-film metal electrode formed on the insulator layer,

characterized in that said electron-supply layer is essentially composed of elements belonging to group IV and has an additive of at least one material selected from atomic elements belonging to group III or V, whereby the electron emission device emits electrons when an electric field is applied between the electron-supply layer and the thin-film metal.

[0013] According to the electron emission device having the structure mentioned above, said insulator layer is made of dielectric and has a film thickness of 50 nm or greater.

[0014] According to the electron emission device having the structure mentioned above, said additive is in-

cluded at a ratio ranging from 0.1 to 10 atm% in the electron-supply layer.

[0015] The present invention further provides an electron emission display device which comprises:

a pair of a first substrate and an optically transparent second substrate opposing to each other with a vacuum space interposed therebetween;  
a plurality of electron emission devices formed on said first substrate, each of which including an electron-supply layer made of semiconductor formed on ohmic electrodes formed on said first substrate, an insulator layer formed on said electron-supply layer, and a thin-film metal electrode formed on said insulator layer and facing the vacuum space, wherein said electron-supply layer is essentially composed of elements belonging to group IV and has an additive of at least one material selected from atomic elements belonging to group III or V;  
a collector electrode formed on said second substrate; and  
a fluorescent material layer formed on said collector electrode and facing the vacuum space.

[0016] In the electron emission display device according to the invention, said insulator layer is made of dielectric and has a film thickness of 50 nm or greater.

[0017] In the electron emission display device according to the invention, said additive is included at a ratio ranging from 0.1 to 10 atm% in the electron-supply layer.

[0018] In the electron emission display device according to the invention, the display device further comprises plural insulative support members formed on said first substrate and disposed between adjacent ones of said electron emission devices so as to enclose the electron emission devices for partitioning them, wherein the distance from said first substrate to the surfaces of said insulative support members proximate to said vacuum space is substantially equal to the distance from said first substrate to the surface of said thin-film metal electrodes proximate to said vacuum space.

[0019] In the electron emission display device according to the invention, the display device further comprises a plurality of bus electrodes, each of which is arranged in a stripe form to electrically connect adjacent ones of said thin-film metal electrodes, wherein said ohmic electrodes and said electrodes are stripe-like electrodes and arranged to extend perpendicular to each other.

[0020] In the electron emission display device according to the invention, said first substrate includes a plurality of first insulative ramparts, each of which disposed between said electron emission devices and protruding into said vacuum space, whereas said second substrate includes a plurality of second ramparts each of which protrudes into said vacuum space to abut to said first ramparts.

[0021] According to the electron emission device of the invention with the above structure, the device com-

prises an electron-supply layer made of semiconductor, particularly elements belonging to group IV and having an additive of at least one material selected from atomic elements belonging to group III or V in the periodic table such as boron (B), phosphorus (P), arsenic (As), antimony (Sb) and so on. Therefore, even when the plural electron emission devices used in a flat panel display device are subjected to a high temperature atmosphere in the manufacturing process, the devices can keep their properties before and after the heating treatment without thermal deterioration due to the high temperature.

[0022] Moreover, through-bores are not likely to be produced in the insulator layer because of its relatively thick thickness and therefore its production yield is improved. The electron emission device of the invention is a planar or spot-like electron emission diode and can be adapted to high speed devices such as a source of a pixel vacuum tube or bulb, an electron emission source of a scanning or transmission electron microscope, a vacuum-micro electronics device and the like. In addition, this electron emission device can serve as a minute microwave tube or a diode which emits electromagnetic waves with millimeter or sub-millimeter wavelength, and also can serve as a high speed switching device.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0023]

Fig. 1 is a schematic cross-sectional view of an electron emission device according to an embodiment of the present invention;

Fig. 2 is a graph showing a relationship between the resistivities of a-Si electron-supply layers of the electron emission devices and the temperatures at which the devices are baked or heated in the heating treatment of the process for manufacturing the electron emission display device according to the invention;

Figs. 3A and 3B are graphs illustrating relationships of the Diode current  $I_d$  and the Emission current  $I_e$  with respect to the Driving voltage  $V_d$  applied to the electron emission device having an electron-supply layer to which a boron additive is added at 0.02 atm% in an embodiment according to the invention before and after the heating treatment respectively; Figs. 4A and 4B are graphs illustrating relationships of the Diode current  $I_d$  and the Emission current  $I_e$  with respect to the Driving voltage  $V_d$  applied to the electron emission device having an electron-supply layer to which a boron additive is added at 0.34 atm% in another embodiment according to the invention before and after the heating treatment respectively;

Figs. 5 to 8 are graphs illustrating relationships of the Diode current  $I_d$  and the Emission current  $I_e$  with respect to the Driving voltage  $V_d$  applied to the electron emission devices having electron-supply

layers to which boron additives are added at 0.15 atm%, 0.34 atm%, 0.44 atm%, and 1.00 atm% in other embodiments according to the invention after the heating treatment respectively;

Fig. 9 is a graph showing a relationship between the emission current  $I_e$  and the film thickness of the insulator layer in the electron emission device according to the invention;

Fig. 10 is a graph showing a relationship between the electron emission efficiency and the film thickness of the insulator layer in the electron emission device according to the invention;

Fig. 11 is a schematic partial perspective view illustrating an electron emission display device according to an embodiment of the present invention; and

Fig. 12 is a schematic partial enlarged cross-sectional view of the electron emission display device according to the present invention, taken with a line AA in Fig. 11.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

**[0024]** An electron emission device according to an embodiment of the present invention will be described in more detail with reference to the accompanying drawings.

**[0025]** As illustrated in Fig. 1, one electron emission device "S" constructed in a stack configuration comprises: an ohmic electrode 11 made of aluminum (Al), tungsten (W), titanium nitride (TiN), copper (Cu), chromium (Cr) or the like on a first substrate 10 made of a glass or the like; an electron-supply layer 12 made of semiconductor such as silicon (Si) or the like on the ohmic electrode 11; an insulator layer 13 made of dielectric such as  $\text{SiO}_x$  ( $x = 0.1$  to  $2.0$ ) or the like on the electron-supply layer 12; and a thin-film metal electrode 15 made of metal such as platinum (Pt), gold (Au) or the like facing a vacuum space on the insulator layer 13. Particularly, the insulator layer 13 is deposited in considerably thicker thicknesses so as to have a thickness of 50 nm or greater. Further, an additive material selected from atomic elements belonging to group III in the periodic table such as boron (B) or from atomic elements belonging to group V in the periodic table such as phosphorus (P), arsenic (As), antimony (Sb) and so on is added to the electron-supply layer 12.

**[0026]** These layer are formed through the sputtering method by using a gas of Ar, Kr or Xe or a mixture thereof, or a gas mixture essentially consisting of one of those rare gases with  $\text{O}_2$ ,  $\text{N}_2$  or the like mixed therein, under the sputtering conditions of a gas pressure of 0.1 to 100 mTorr, preferably 0.1 to 20 mTorr and the forming rate of 0.1 to 1000 nm/min, preferably 0.5 to 100 nm/min.

**[0027]** In case that the first substrate 10 carrying plural electron emission devices i.e., device substrate or back substrate is used for a display device, a second substrate i.e., front substrate 1 made of glass and the

substrate 10 are supported apart from one another in parallel by a spacer and sealed to face each other with a vacuum space 4 interposed therebetween. On the inner surface of the front substrate 1, at least one transparent collector electrode 2 of, for example, an indium tin oxide (so-called ITO), tin oxide (SnO), zinc oxide (ZnO) or the like, is formed. The transparent collector electrode traps emitted electrons. The transparent collector electrodes may be arranged in groups of three in association with red (R), green (G) and blue (B) color signals in order to provide a color display panel, and voltages are applied to those three collector electrodes respectively. The transparent collector electrodes are separately coated with fluorescent materials 3R, G, B which are capable of emitting lights of R, G and B corresponding to the three primary colors of light respectively. A black stripe masks BM or back metal layer may be provided on the front substrate 1 between the fluorescent material layers 3R, G, B. Alternatively the collector electrode 2 may be formed integrally as a monolithic film for a monochrome display panel. The material for the back substrate 10 is not limited to glass, and ceramics such as  $\text{Al}_2\text{O}_3$ ,  $\text{Si}_3\text{N}_4$  and BN etc. may be used instead of glass.

**[0028]** This electron emission device "S" can be regarded as a diode of which the thin-film metal electrode 15 at its surface is connected to a positive applied voltage  $V_d$  and the ohmic electrode 11 is connected to a ground potential as shown in Fig. 1. When the voltage  $V_d$  for example approximately 90 volt is applied between the ohmic electrode 11 and the thin-film metal electrode 15 to supply electrons into the electron-supply layer 12, a diode current  $I_d$  flows. Since the insulator layer 13 has a high resistance, most of the applied electric field is applied and concentrated to the insulator layer 13. The electrons travel in the insulator layer 13 toward the thin-film metal electrode 15. Some of the electrons that reach near the thin-film metal electrode 15 pass through the thin-film metal electrode 15, due to the strong electric field, to be emitted out into the vacuum space.

**[0029]** The electrons "e" (emission current  $I_e$ ) discharged from the thin-film metal electrode 15 are soon accelerated by a high voltage  $V_c$  for example approximately 5 kilovolt which is applied to an opposing collector electrode 2 (transparent electrode), and are collected with the collector electrode 2. When a fluorescent substance 3 is coated on the collector electrode 2, corresponding visible light is observed.

**[0030]** An effective main component material for the electron-supply layer 12 of the electron emission device is silicon (Si) and particularly amorphous silicon (so-called a-Si), further particularly the hydrogenated amorphous silicon (a-Si:H) in which almost of the dangling bonds of a-Si are terminated by hydrogen atoms is effective. In addition, hydrogenated amorphous silicon carbide (a-SiC:H) in which parts of Si atoms are replaced by carbon atoms (C) is also effectively used for the electron-supply layer 12. Moreover hydrogenated amorphous silicon nitride (a-SiN:H) in which parts of Si

atoms are replaced by nitrogen atoms (N) may be also effectively used for the electron-supply layer 12. In addition, silicon doped with boron, aluminium, gallium, phosphorus, indium, arsenic and/or antimony may be used for the electron-supply layer 12. In addition to Si, an elemental semiconductor or a compound semiconductor of an element of a group IV such as a germanium (Ge), germanium silicon compound (Ge-Si), silicon carbide (SiC) is also used as a main component for the electron-supply layer 12 of the electron emission device as well.

**[0031]** The electron-supply layer 12 contains the additive material selected from atomic elements consisting of B, Al, Ga, In and Tl belonging to group IIIb in the periodic table and P, As, Sb and Bi belonging to group Vb in the periodic table at a ratio ranging from 0.1 to 10 atm%. The added additive material to the electron-supply layer 12 increases donors and acceptors to raise the density of carriers. It can be understood that, by this addition of additive to the electron-supply layer, the descent of electric resistance caused by increasing of dangling bonds of Si is offset.

**[0032]** Silicon oxide  $\text{SiO}_x$  (wherein subscribed x represents an atomic ratio) is effective as the dielectric material of and, metal oxides or metal nitrides such as  $\text{LiO}_x$ ,  $\text{LiN}_x$ ,  $\text{NaO}_x$ ,  $\text{KO}_x$ ,  $\text{RbO}_x$ ,  $\text{CsO}_x$ ,  $\text{BeO}_x$ ,  $\text{MgO}_x$ ,  $\text{MgN}_x$ ,  $\text{CaO}_x$ ,  $\text{CaN}_x$ ,  $\text{SrO}_x$ ,  $\text{BaO}_x$ ,  $\text{ScO}_x$ ,  $\text{YO}_x$ ,  $\text{YN}_x$ ,  $\text{LaO}_x$ ,  $\text{LaN}_x$ ,  $\text{CeO}_x$ ,  $\text{PrO}_x$ ,  $\text{NdO}_x$ ,  $\text{SmO}_x$ ,  $\text{EuO}_x$ ,  $\text{GdO}_x$ ,  $\text{TbO}_x$ ,  $\text{DyO}_x$ ,  $\text{HoO}_x$ ,  $\text{ErO}_x$ ,  $\text{TmO}_x$ ,  $\text{YbO}_x$ ,  $\text{LuO}_x$ ,  $\text{TiO}_x$ ,  $\text{ZrO}_x$ ,  $\text{ZrN}_x$ ,  $\text{HfO}_x$ ,  $\text{HfN}_x$ ,  $\text{ThO}_x$ ,  $\text{VO}_x$ ,  $\text{VN}_x$ ,  $\text{NbO}_x$ ,  $\text{TaO}_x$ ,  $\text{TaN}_x$ ,  $\text{CrO}_x$ ,  $\text{CrN}_x$ ,  $\text{MoO}_x$ ,  $\text{MoN}_x$ ,  $\text{WO}_x$ ,  $\text{WN}_x$ ,  $\text{MnO}_x$ ,  $\text{ReO}_x$ ,  $\text{FeO}_x$ ,  $\text{FeN}_x$ ,  $\text{RuO}_x$ ,  $\text{OsO}_x$ ,  $\text{CoO}_x$ ,  $\text{RhO}_x$ ,  $\text{IrO}_x$ ,  $\text{NiO}_x$ ,  $\text{PdO}_x$ ,  $\text{PtO}_x$ ,  $\text{CuO}_x$ ,  $\text{CuN}_x$ ,  $\text{AgO}_x$ ,  $\text{AuO}_x$ ,  $\text{ZnO}_x$ ,  $\text{CdO}_x$ ,  $\text{HgO}_x$ ,  $\text{BO}_x$ ,  $\text{BN}_x$ ,  $\text{AlO}_x$ ,  $\text{AlN}_x$ ,  $\text{GaO}_x$ ,  $\text{GaN}_x$ ,  $\text{InO}_x$ ,  $\text{SiN}_x$ ,  $\text{GeO}_x$ ,  $\text{SnO}_x$ ,  $\text{PbO}_x$ ,  $\text{PO}_x$ ,  $\text{PN}_x$ ,  $\text{AsO}_x$ ,  $\text{SbO}_x$ ,  $\text{SeO}_x$ ,  $\text{TeO}_x$  and the like can be used as well for the insulator layer 13.

**[0033]** Furthermore, metal complex oxides such  $\text{LiAlO}_2$ ,  $\text{Li}_2\text{SiO}_3$ ,  $\text{Li}_2\text{TiO}_3$ ,  $\text{Na}_2\text{Al}_{22}\text{O}_{34}$ ,  $\text{NaFeO}_2$ ,  $\text{Na}_4\text{SiO}_4$ ,  $\text{K}_2\text{SiO}_3$ ,  $\text{K}_2\text{TiO}_3$ ,  $\text{K}_2\text{WO}_4$ ,  $\text{Rb}_2\text{CrO}_4$ ,  $\text{Cs}_2\text{CrO}_4$ ,  $\text{MgAl}_2\text{O}_4$ ,  $\text{MgFe}_2\text{O}_4$ ,  $\text{MgTiO}_3$ ,  $\text{CaTiO}_3$ ,  $\text{CaWO}_4$ ,  $\text{CaZrO}_3$ ,  $\text{SrFe}_{12}\text{O}_{19}$ ,  $\text{SrTiO}_3$ ,  $\text{SrZrO}_3$ ,  $\text{BaAl}_2\text{O}_4$ ,  $\text{BaFe}_{12}\text{O}_{19}$ ,  $\text{BaTiO}_3$ ,  $\text{Y}_3\text{Al}_5\text{O}_{12}$ ,  $\text{Y}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{LaFeO}_3$ ,  $\text{La}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{La}_2\text{Ti}_2\text{O}_7$ ,  $\text{CeSnO}_4$ ,  $\text{CeTiO}_4$ ,  $\text{Sm}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{EuFeO}_3$ ,  $\text{Eu}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{GdFeO}_3$ ,  $\text{Gd}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{DyFeO}_3$ ,  $\text{Dy}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{HoFeO}_3$ ,  $\text{Ho}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{ErFeO}_3$ ,  $\text{Er}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{Tm}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{LuFeO}_3$ ,  $\text{Lu}_3\text{Fe}_5\text{O}_{12}$ ,  $\text{NiTiO}_3$ ,  $\text{Al}_2\text{TiO}_3$ ,  $\text{FeTiO}_3$ ,  $\text{BaZrO}_3$ ,  $\text{LiZrO}_3$ ,  $\text{MgZrO}_3$ ,  $\text{HfTiO}_4$ ,  $\text{NH}_4\text{VO}_3$ ,  $\text{AgVO}_3$ ,  $\text{LiVO}_3$ ,  $\text{BaNb}_2\text{O}_6$ ,  $\text{NaNbO}_3$ ,  $\text{SrNb}_2\text{O}_6$ ,  $\text{KTaO}_3$ ,  $\text{NaTaO}_3$ ,  $\text{SrTa}_2\text{O}_6$ ,  $\text{CuCr}_2\text{O}_4$ ,  $\text{Ag}_2\text{CrO}_4$ ,  $\text{BaCrO}_4$ ,  $\text{K}_2\text{MoO}_4$ ,  $\text{Na}_2\text{MoO}_4$ ,  $\text{NiMoO}_4$ ,  $\text{BaWO}_4$ ,  $\text{Na}_2\text{WO}_4$ ,  $\text{SrWO}_4$ ,  $\text{MnCr}_2\text{O}_4$ ,  $\text{MnFe}_2\text{O}_4$ ,  $\text{MnTiO}_3$ ,  $\text{MnWO}_4$ ,  $\text{CoFe}_2\text{O}_4$ ,  $\text{ZnFe}_2\text{O}_4$ ,  $\text{FeWO}_4$ ,  $\text{CoMoO}_4$ ,  $\text{CoTiO}_3$ ,  $\text{CoWO}_4$ ,  $\text{NiFe}_2\text{O}_4$ ,  $\text{NiWO}_4$ ,  $\text{CuFe}_2\text{O}_4$ ,  $\text{CuMoO}_4$ ,  $\text{CuTiO}_3$ ,  $\text{CuWO}_4$ ,  $\text{Ag}_2\text{MoO}_4$ ,  $\text{Ag}_2\text{WO}_4$ ,  $\text{ZnAl}_2\text{O}_4$ ,  $\text{ZnMoO}_4$ ,  $\text{ZnWO}_4$ ,  $\text{CdSnO}_3$ ,  $\text{CdTiO}_3$ ,  $\text{CdMoO}_4$ ,  $\text{CdWO}_4$ ,  $\text{NaAlO}_2$ ,  $\text{MgAl}_2\text{O}_4$ ,  $\text{SrAl}_2\text{O}_4$ ,  $\text{Gd}_3\text{Ga}_5\text{O}_{12}$ ,  $\text{InFeO}_3$ ,  $\text{MgIn}_2\text{O}_4$ ,  $\text{Al}_2\text{TiO}_5$ ,  $\text{FeTiO}_3$ ,  $\text{MgTiO}_3$ ,  $\text{NaSiO}_3$ ,  $\text{CaSiO}_3$ ,  $\text{ZrSiO}_4$ ,  $\text{K}_2\text{GeO}_3$ ,  $\text{Li}_2\text{GeO}_3$ ,  $\text{Na}_2\text{GeO}_3$ ,  $\text{Bi}_2\text{Sn}_3\text{O}_9$ ,  $\text{MgSnO}_3$ ,  $\text{SrSnO}_3$ ,

$\text{PbSiO}_3$ ,  $\text{PbMoO}_4$ ,  $\text{PbTiO}_3$ ,  $\text{SnO}_2\text{-Sb}_2\text{O}_3$ ,  $\text{CuSeO}_4$ ,  $\text{Na}_2\text{SeO}_3$ ,  $\text{ZnSeO}_3$ ,  $\text{K}_2\text{TeO}_3$ ,  $\text{K}_2\text{TeO}_4$ ,  $\text{Na}_2\text{TeO}_3$ ,  $\text{Na}_2\text{TeO}_4$  and the like can be used as well for the insulator layer 13. And still furthermore, sulfides such as  $\text{FeS}$ ,  $\text{Al}_2\text{S}_3$ ,  $\text{MgS}$ ,  $\text{ZnS}$  and the like, fluorides such as  $\text{LiF}$ ,  $\text{MgF}_2$ ,  $\text{SmF}_3$  and the like, chlorides such as  $\text{HgCl}$ ,  $\text{FeCl}_2$ ,  $\text{CrCl}_3$  and the like, bromides such as  $\text{AgBr}$ ,  $\text{CuBr}$ ,  $\text{MnBr}_2$  and the like, iodide such as  $\text{PbI}_2$ ,  $\text{CuI}$ ,  $\text{FeI}_2$  and the like and metal oxidized nitrides such as  $\text{SiAlON}$  and the like can be used as well for the insulator layer 13.

**[0034]** Moreover, carbon such as diamond, Fullerene ( $\text{C}_{2n}$ ) and the like or metal carbide such as  $\text{Al}_4\text{C}_3$ ,  $\text{B}_4\text{C}$ ,  $\text{CaC}_2$ ,  $\text{Cr}_3\text{C}_2$ ,  $\text{Mo}_2\text{C}$ ,  $\text{MoC}$ ,  $\text{NbC}$ ,  $\text{SiC}$ ,  $\text{TaC}$ ,  $\text{TiC}$ ,  $\text{VC}$ ,  $\text{W}_2\text{C}$ ,  $\text{WC}$ ,  $\text{ZrC}$  and the like are also effective as the dielectric material of the insulator layer 13. Fullerene ( $\text{C}_{2n}$ ) consists of carbon atoms. The representative  $\text{C}_{60}$  is a spherical surface basket molecule as known a soccer ball molecule. There is also known  $\text{C}_{32}$  to  $\text{C}_{960}$  and the like. The subscribed x in  $\text{O}_x$ ,  $\text{N}_x$  and the like in the above chemical formulas represent atomic ratios and also herein after.

**[0035]** The film thickness of the insulator layer 13 may be 50 nm or greater preferably in ranging from 100 to 1000 nm.

**[0036]** Metals Pt, Au, W, Ru and Ir are effective as the material for the thin-film metal electrode 15 on the electron emission side. In addition, Be, C, Al, Si, Sc, Ti, V, Cr, Mn, Fe, Co, Ni, Cu, Zn, Ga, Y, Zr, Nb, Mo, Tc, Rh, Pd, Ag, Cd, In, Sn, Ta, Re, Os, Tl, Pb, La, Ce, Pr, Nd, Pm, Sm, Eu, Gd, Tb, Dy, Ho, Er, Tm, Yb, Lu and the like can be used as well for the thin-film metal electrode.

**[0037]** Although sputtering is particularly effective in the fabrication of those layers and the substrate, vacuum deposition, CVD (Chemical Vapor Deposition), laser ablation, MBE (Molecular Beam Epitaxy) and ion beam sputtering are also effective.

**[0038]** Electron emission devices according to the invention each comprising an electron-supply layer made of Si to which boron was added were fabricated as embodiments and their characteristics were examined concretely.

**[0039]** First, an ohmic electrode of 300 nm thick of TiN was deposited on each of plural back substrates of glass by sputtering. Various electron-supply layers of a-Si to which boron (B) were added at rates of 0.02 atm%, 0.15 atm%, 0.340 atm%, 0.44 atm%, 1.00 atm% and 10.00 atm% were deposited with a thickness of 5.0  $\mu\text{m}$ , by sputtering, on the electrode surfaces of the substrates respectively. Then, an insulator layer of  $\text{SiO}_x$  was deposited with a thickness of 400 nm on the electron-supply layer of each substrate. Finally, a thin-film metal electrode of Pt was deposited with a thickness of 10 nm on the surface of the amorphous  $\text{SiO}_x$  layer of each substrate by sputtering, thus providing plural device substrates.

**[0040]** Meanwhile, transparent substrates of glass were prepared as front substrates, on each of which an ITO collector electrode was firstly formed on a transpar-

ent glass substrate and a fluorescent layer of a fluorescent substance corresponding to color emission was secondly formed on the collector electrode.

**[0041]** Electron emission devices were assembled in each of which the device substrate and the front substrate were supported apart from one another by 10 mm in parallel by a spacer in such a way that the thin-film metal electrode faced the collector electrode, with the clearance therebetween made to a vacuum of  $10^{-7}$  Torr or  $10^{-5}$  Pa, thus providing plural electron emission display devices. The electric properties of the electron emission devices were measured at room temperature.

**[0042]** Next, the plural resultant devices were heated or baked at temperatures of 200°C, 300°C, 400°C, 500°C and 600°C in a vacuum atmosphere for one hour respectively in order to compare the properties between the electron emission devices at a 25°C (room temperature), i.e., non-heated devices and the devices heated at various temperatures and then cooled.

**[0043]** Then, after heating treatment of devices, they were cooled down at room temperature. Then, the resistivities of the heated and cooled plural devices were measured correspondingly to the baked temperatures. Fig. 2 shows the relationship between the resistivities of a-Si electron-supply layers of the resultant devices and the temperatures at which the device were baked or heated in the heating treatments of the manufacturing process. In Fig. 2, the plots of ● represent the resistivities of the devices each having the a-Si electron-supply layer to which boron is added at 0.02 atm%, in which the resistivities thereof rapidly increase as the baked temperature rises. The plots of ■, ▲ and ◆ represent the resistivities of the a-Si electron-supply layers to which boron are added at 0.15 atm%, 0.34 atm% and 0.44 atm% respectively, in which these devices apparently keep their resistivities restricted even after heating of the 300°C temperature or more.

**[0044]** Further, the Diode current  $I_d$  and Emission current  $I_e$  of the device before and after the heating treatments (25°C and 300°C) were measured, while a driving voltage  $V_{ps}$  ranging from -50 to 110V is applied between the thin-film metal electrode and the ohmic electrode of each device having the electron-supply layer to which boron is added. For example, Figs. 3A and 3B illustrate the resultant measured characteristics of the device having the electron-supply layer to which boron is added at 0.02 atm% corresponding to plots ● in Fig. 2 obtained before and after the heating treatments respectively. And Fig. 4A and 4B illustrate the resultant measured characteristics of the device having the electron-supply layer to which boron is added at 0.34 atm% corresponding to plots ▲ in Fig. 2 obtained before and after the heating treatments respectively. In these figures, the curvatures rising at about 80V represent characteristics of Emission current, and the other curvatures represent characteristics of Diode current. Figs. 3A and 4A show characteristics of the devices measured before the heating treatments respectively. Figs. 3B and 4B

show characteristics of the devices measured after the heating treatments respectively. As seen from Figs. 3A and 3B, it is understood that the diode current hardly flows because the electron-supply layer has a high electric resistance due to the heating treatment in case that the density of added Boron to the electron-supply layer is low. On the other hand, as seen from Fig. 4A and 4B, in case that the density of added Boron to the electron-supply layer is higher than 0.02 atm%, the electron emission devices can keep substantially the same properties before and after the heating treatment.

**[0045]** Moreover, the devices having the electron-supply layers to which boron were added at the density rates of 0.15 atm%, 0.34 atm%, 0.44 atm% and 1.00 atm% were heated at a temperature 400°C for one hour respectively, and after that, the devices were cooled down at room temperature. Then, Diode current  $I_d$  and Emission current  $I_e$  of the resultant devices were measured in such a manner that a driving voltage ranging from 0 to 110V is applied between the thin-film metal electrode and the ohmic electrode of the each device. Fig. 5, Fig. 6, Fig. 7 and Fig. 8 show the resultant values  $I_d$  and  $I_e$  of the devices having the electron-supply layers to which boron were added at the density rates of 0.15 atm%, 0.34 atm%, 0.44 atm% and 1.00 atm% respectively. In these figures, plots ○ rising at about 90V, 80V, 70V and 40V represent Emission current characteristics respectively, and the other plots ● represent Diode current characteristics. As seen from these figures, it is understood that the electron emission devices each having the boron added electron-supply layer and heated at 400°C for one hour and then cooled can keep substantially the same properties before and after the heating treatment similarly to the heating treatment of the device preformed at 300°C. In addition, it was confirmed that the electron emission devices each having boron added at a density rate of 10.00 atm% and heated at 400°C for one hour and then cooled can keep substantially the same properties before and after the heating treatment, by means of the measurement of Diode current  $I_d$  and Emission current  $I_e$  that a driving voltage ranging from 0 to 110V is applied each device electron-supply layer.

**[0046]** Next, the second embodiments of electron emission devices each having the electron-supply layer to which boron was added at a density rate of 0.44 atm% were fabricated in the same manner as the first embodiments excepting that an insulator layer 13 of  $\text{SiO}_x$  was deposited while changing the film thickness thereof in a range of 25 nm, 50 nm, 100 nm, 200 nm, 300 nm, 400 nm, 500 nm, 600 nm, 800 nm, 1000 nm. Then, the electron emission devices of the second embodiments were heated or baked at temperatures of 400°C in a vacuum atmosphere for one hour and then cooled down to room temperature respectively. After that, the emission current  $I_e$  and the electron emission efficiency ( $I_e/I_d$ ) in the heated and cooled plural devices were measured correspondingly to the thicknesses of insulator layer, when

a driving voltage  $V_d$  of 110 V was applied to the prepared electron emission devices. The results are illustrated in Figs. 9 and 10 showing the variations of the emission current  $I_e$  and the electron emission efficiency ( $I_e/I_d$ ) respectively with respect to the film thickness ranging from 25 nm to 1000 nm of the insulator layer of the devices. As apparent from Fig. 9, the devices whose insulator layer had a thickness of from 50 nm to 1000 nm shows the emission current of about  $1 \times 10^{-6}$  A/cm<sup>2</sup> or more. As apparent from Fig. 10, the devices whose insulator layer had a thickness of from 50 nm to 1000 nm shows the electron emission efficiency of about  $1 \times 10^{-3}$ .

[0047] There were observations of the surface of the  $\text{SiO}_x$  insulator layer resulted from the oxidation of the electron supply layer by a scanning electron microscope (SEM) during the above formation process, grain surface each having an about 20 nm diameter appeared. The grain structure of  $\text{SiO}_x$  of the insulator layer seems to cause the peculiar phenomenon that the passing current flows through the insulator layer which has a thickness of 50 nm or greater.

[0048] Fig. 11 illustrates an embodiment of an electron emission display device comprising ramparts or ribs are formed therein and a plurality of electron emission devices arranged in a matrix form as an image display array. The illustrated electron emission display device comprises a pair of a front substrate 1 of optically transparent and a back substrate 10 made of glass or the like. Rear ramparts or ribs RR<sub>s</sub> formed on the back substrate 10 abut to front ramparts or ribs FR<sub>s</sub> formed on the front substrate 1 at the top surfaces thereof so that both the substrates are placed apart from and parallel to each other with a vacuum space 4 interposed therebetween.

[0049] A plurality of ohmic electrodes 11 extending in parallel with each other are formed on the inner surface of the back substrate 10 proximately to the vacuum space 4. The ohmic electrodes 11 are grouped into sets of three corresponding to R (red), G (green), B (blue) color signals to provide a color display panel, and are applied with predetermined signals, respectively. On the ohmic electrodes 11, a plurality of electron emission devices S are formed and arranged in a matrix form. On portions of thin-film metal electrodes of adjacent devices, a plurality of bus electrodes 16 extending perpendicular to the ohmic electrodes and in parallel with each other are routed to electrically connect the thin-film metal electrodes. An intersection of an ohmic electrode 11 and a bus electrode 16 corresponds to an electron emission device S. Therefore, a passive matrix scheme or an active matrix scheme may be applied as a driving scheme for the display apparatus of the present invention.

[0050] As illustrated in Fig. 12, each electron emission device S comprises an electron-supply layer 12, an insulator layer 13 and a thin-film metal electrode 15 formed in this order on the ohmic electrode 11. The thin-film metal electrode 15 faces the vacuum space 4. The

electron-supply layer is essentially composed of elements belonging to group IV such as Si and has an additive of at least one material selected from atomic elements belonging to group III such as B at a ratio ranging from 0.1 to 10 atm%.

[0051] In particular, an insulative support member 17 is formed to surround each of the electron emission devices S and to define a plurality of electron emitting regions. The insulative support member 17 supports the bus electrodes 16, and prevents line failures. More specifically, as illustrated in Fig. 12, the insulative support member 17 or a material having a large electric resistance is previously formed in peripheral portions except for the electron emission devices in a thickness similar to a final thickness of the electron emission devices when formed in subsequent steps.

[0052] Further, in this embodiment, the rear ribs RR<sub>s</sub> associated with the back substrate 10 is formed on the insulative support member 17 so as to protrude into the vacuum space 4 from the back substrate 10. The ribs RR<sub>s</sub> are positioned at predetermined intervals. While in Fig. 11, the ribs RR<sub>s</sub> are formed between respective electron emission devices S, the ribs RR<sub>s</sub> may be formed at larger intervals, for example, every two or three electron emission devices S. Also, while in Fig. 11, the ribs RR<sub>s</sub> are continuously formed in a direction substantially perpendicular to the ohmic electrodes 11, the ribs RR<sub>s</sub> may be intermittently formed so as to leave upper areas including portions abutting to the front ramparts FR<sub>s</sub> associated with the front substrate 1, but not illustrated.

[0053] Preferably, each of the ribs RR<sub>s</sub> has its top surface area larger than the bottom surface area which is in contact with the back substrate 10. In other words, each of the ribs RR<sub>s</sub> is preferably formed to have an overhanging portion on its top which protrudes in a direction substantially parallel with the back substrate 10.

[0054] Further, while in Fig. 11, the bus electrodes 16 disposed on the thin-film metal electrodes 15 of the back substrate 10 are formed in a simple linear shape, the bus electrodes 16 are preferably formed, instead of the linear shape, so as to have a width between the thin-film metal electrodes 15 of the electron emission devices larger than that on the thin-film metal electrodes. In other words, the bus electrodes 16 are preferably formed to be wider between the electron emission devices than on the devices. In this way, the resistance of the bus electrodes can be reduced.

[0055] The material for the ohmic electrodes 11 is Au, Pt, Al, W or the like which is generally used for the wires in an IC, and alternatively the ohmic electrodes may be formed as a three layer structure composed of chromium, nickel and chromium layers. Moreover, an alloy of Al-Nd, Al-Mo, or Ti-N may be used for the ohmic electrode material. The ohmic electrodes 11 has a uniform thickness for supplying substantially the same current to the individual devices. In addition, an auxiliary insulator layer comprised of an insulator such as  $\text{SiO}_2$ ,  $\text{SiN}_x$ ,

Al<sub>2</sub>O<sub>3</sub> or AlN may be provided between the back substrate 10 and the ohmic electrode 11, but not shown in Fig. 11. The auxiliary insulator layer serves to prevent an adverse influence of the back substrate 10 on the device (such as elution of an impurity such as an alkaline component or a roughened substrate surface).

[0056] From the principle of electron emission, it is better that the material for the thin-film metal electrode 15 has a lower work function  $\phi$  and is thinner. To increase the electron emission efficiency, the material for the thin-film metal electrode 15 should be a metal of the group I or group II in the periodic table; for example, Cs, Rb, Li, Sr, Mg, Ba, Ca and the like are effective and alloys of those elements may be used as well. To make the thin-film metal electrode 15 very thin, the material for the thin-film metal electrode 15 should be chemically stable with a high conductivity; for example, single substances of Au, Pt, Lu, Ag, Cu and Ir or an alloy thereof or a multi-layer thereof are desirable. It is effective to coat or dope a metal with a low work function as described above on or in those metals.

[0057] The material for the bus electrodes 16 can be Au, Pt, Al or the like which is generally used for the wiring of an integrated circuit IC, and should have a thickness enough to supply substantially the same potential to the individual devices, adequately of 0.1 to 50  $\mu\text{m}$ . If the electrical resistance of material of the thin-film metal electrode is allowable for the bus electrodes, such electrode material may be used for the bus electrodes.

[0058] On the other hand, transparent collector electrodes 2 made of ITO to which a high voltage is applied are integrally formed on the inner surface (surface opposing to the back substrate 10) of the optically transparent front substrate 1 made of transparent glass and serving as a display surface. In case that a black stripe mask or back metal layer is provided on the front substrate 1, such a conductive layer may be serve as a collector electrode instead of the ITO transparent collector electrode.

[0059] On the collector electrodes 2, a plurality of front ribs (second insulative rib) FRs are formed in parallel with the ohmic electrodes 11. On the collector electrodes 2 between the extending front ribs, fluorescent material layers 3R, 3G, 3B made of fluorescent materials corresponding to R, G, B are formed, respectively, in opposition to the vacuum space 4. In this way, the front ribs (second insulative rib) FRs are provided at boundaries of the respective fluorescent materials to maintain a constant distance (for example, 1 mm) between the back substrate and the front substrate. Since the front ribs (second insulative rib) FRs are disposed on the front substrate 1 in a direction orthogonal to the rear ribs (first insulative rib) RR disposed on the back substrate 10, it is ensured that the front substrate are definitely colored by the fluorescent materials in R, G, B corresponding to the three primary colors of light.

[0060] As described above, the electron emission display device according to this embodiment has an image

display array composed of a plurality of light emitting pixels which are arranged in a matrix form and each of which includes red (R), green (G) and blue (B) light emitting elements. Of course, a monochrome display panel may be formed by replacing all the RGB light emitting elements with monochrome light emitting elements.

## Claims

### 1. An electron emission device comprising:

an electron-supply layer made of semiconductor;  
an insulator layer formed on the electron-supply layer; and  
a thin-film metal electrode formed on the insulator layer;

characterized in that said electron-supply layer is essentially composed of elements belonging to group IV and has an additive of at least one material selected from atomic elements belonging to group III or V, whereby the electron emission device emits electrons when an electric field is applied between the electron-supply layer and the thin-film metal.

### 2. An electron emission device according to claim 1, wherein said insulator layer is made of dielectric and has a film thickness of 50 nm or greater.

### 3. An electron emission device according to claim 1, wherein said additive is included at a ratio ranging from 0.1 to 10 atm% in the electron-supply layer.

### 4. An electron emission display device comprising:

a pair of a first substrate and an optically transparent second substrate opposing to each other with a vacuum space interposed therebetween;

a plurality of electron emission devices formed on said first substrate, each of which including an electron-supply layer made of semiconductor formed on ohmic electrodes formed on said first substrate, an insulator layer formed on said electron-supply layer, and a thin-film metal electrode formed on said insulator layer and facing the vacuum space, wherein said electron-supply layer is essentially composed of elements belonging to group IV and has an additive of at least one material selected from atomic elements belonging to group III or V;

a collector electrode formed on said second substrate; and

a fluorescent material layer formed on said collector electrode and facing the vacuum space.



5. An electron emission display device according to claim 4, wherein said insulator layer is made of dielectric and has a film thickness of 50 nm or greater.
6. An electron emission display device according to claim 4, wherein said additive is included at a ratio ranging from 0.1 to 10 atm% in the electron-supply layer.
7. An electron emission display device according to claim 4 further comprising plural insulative support members formed on said back substrate and disposed between adjacent ones of said electron emission devices so as to enclose the electron emission devices for partitioning them, wherein the distance from said back substrate to the surfaces of said insulative support members proximate to said vacuum space is substantially equal to the distance from said back substrate to the surface of said thin-film metal electrodes proximate to said vacuum space.
8. An electron emission display device according to claim 4 further comprising a plurality of bus electrodes, each of which is arranged in a stripe form to electrically connect adjacent ones of said thin-film metal electrodes, wherein said ohmic electrodes and said electrodes are stripe-like electrodes and arranged to extend perpendicular to each other.
9. An electron emission display device according to claim 4, wherein said back substrate includes a plurality of first insulative ramparts, each of which disposed between said electron emission devices and protruding into said vacuum space, whereas said front substrate includes a plurality of second ramparts each of which protrudes into said vacuum space to abut to said first ramparts.

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FIG.1

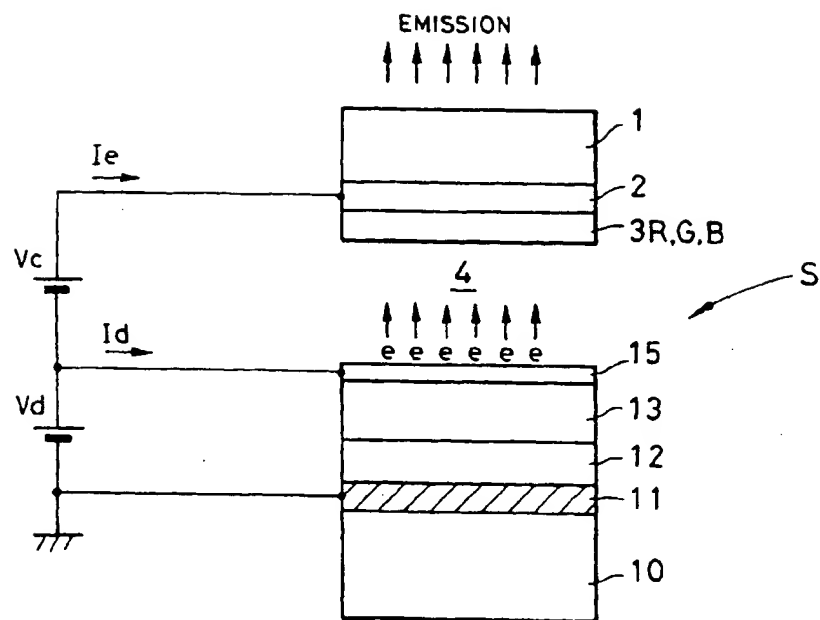


FIG. 2

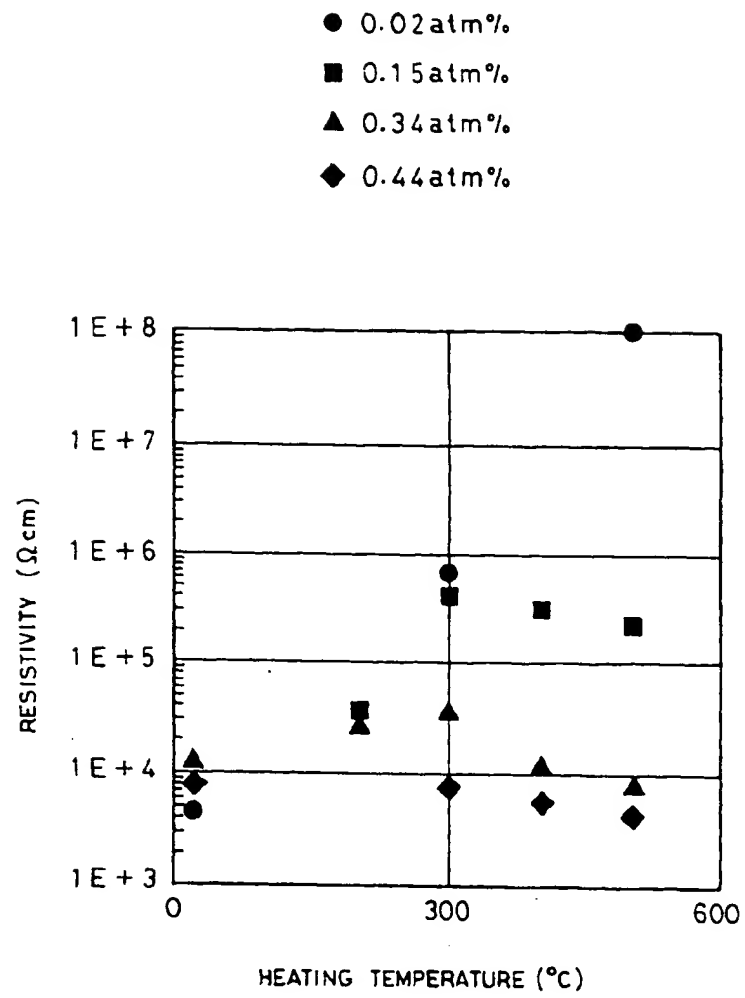


FIG.3A

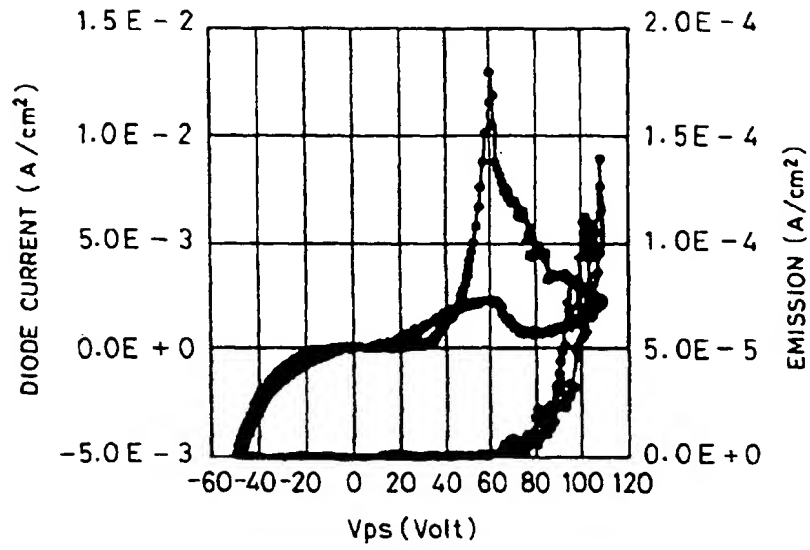


FIG.3B

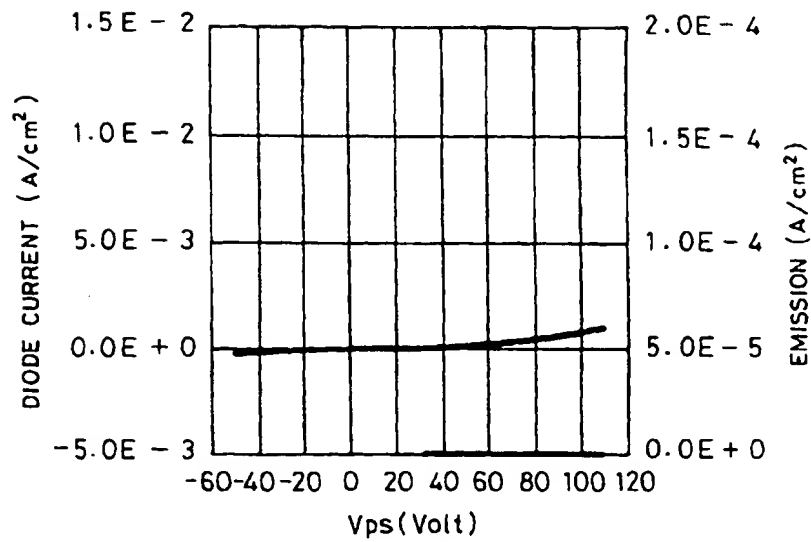


FIG. 4A

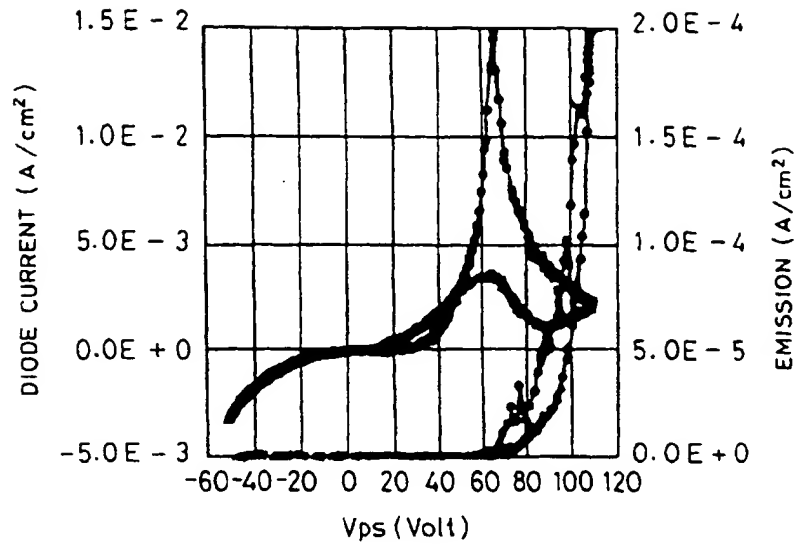


FIG. 4B

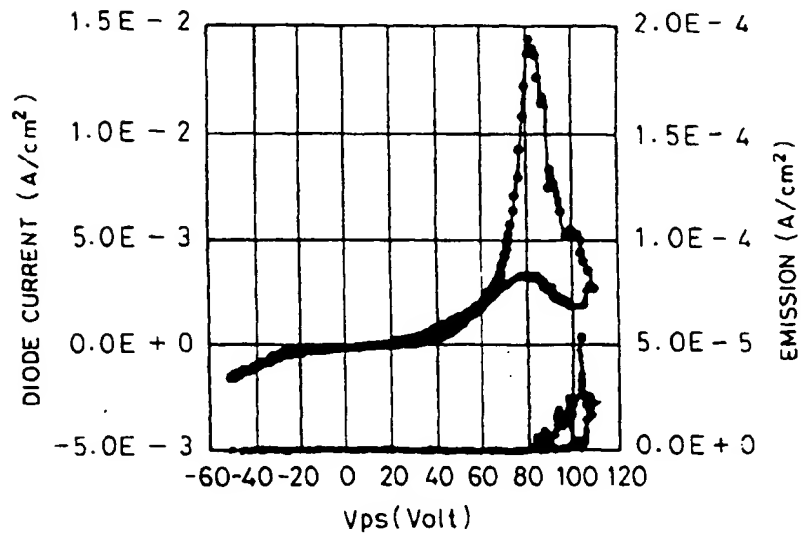


FIG. 5

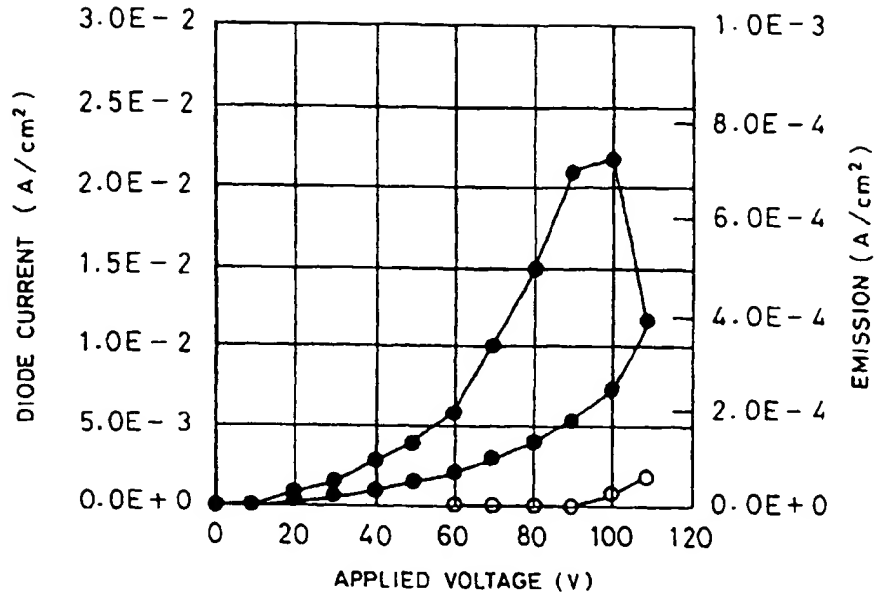


FIG. 6

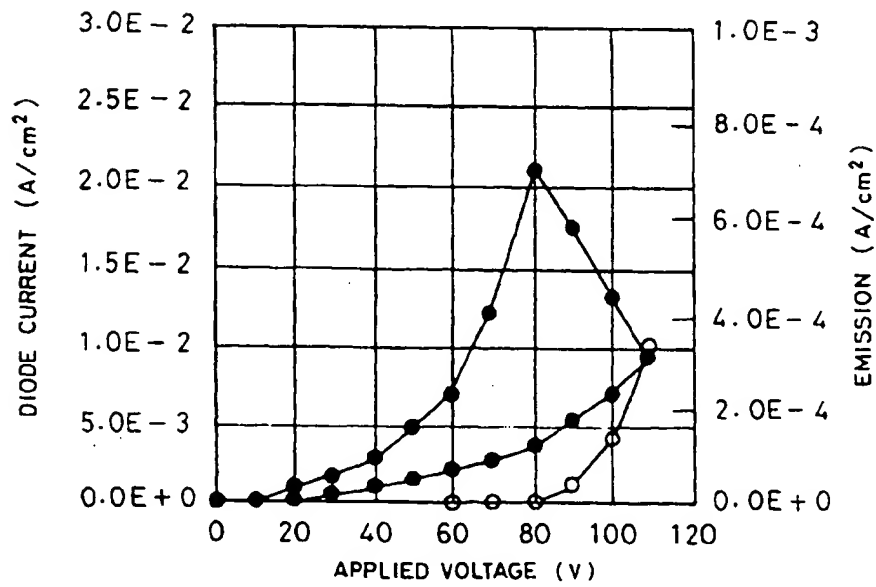


FIG. 7

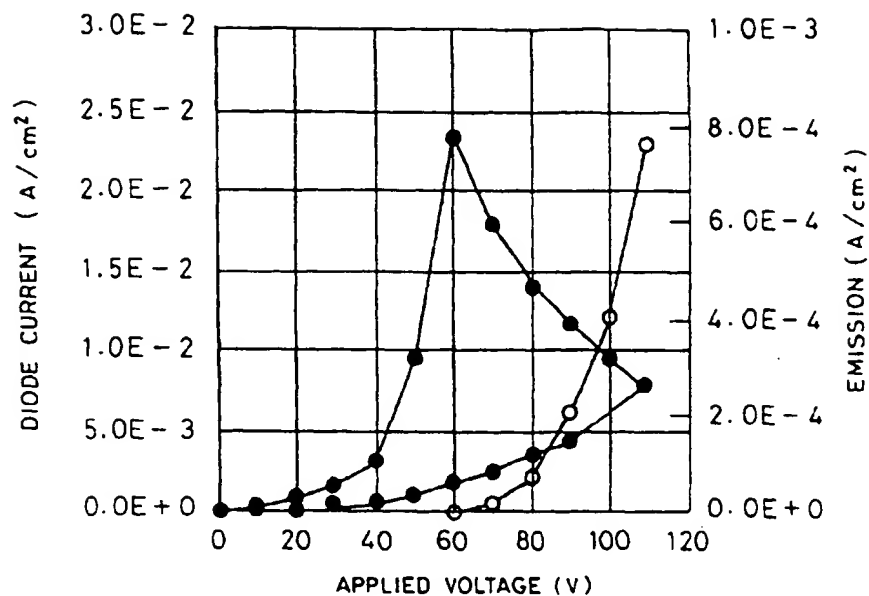


FIG. 8

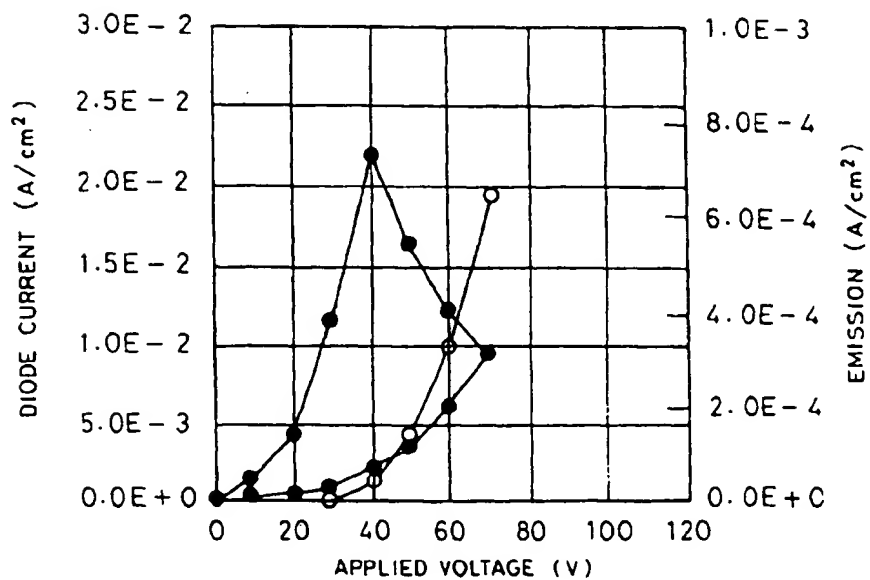


FIG. 9

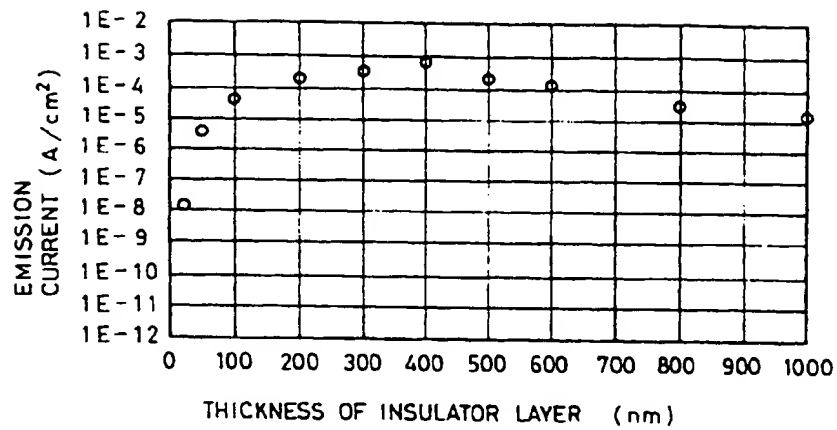


FIG. 10

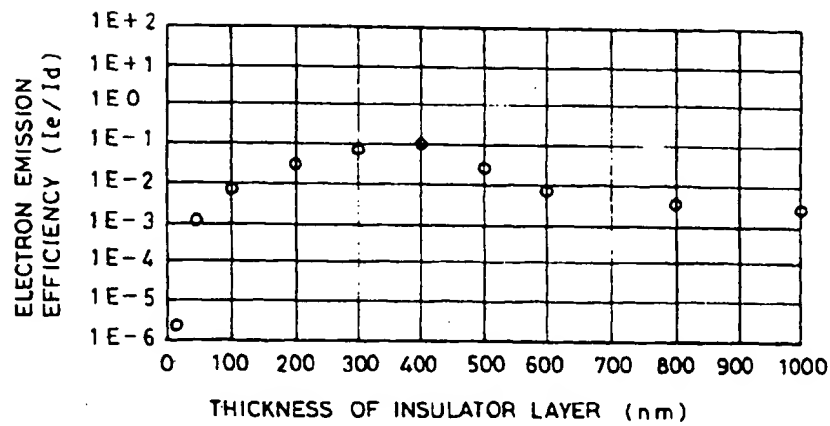




FIG.11

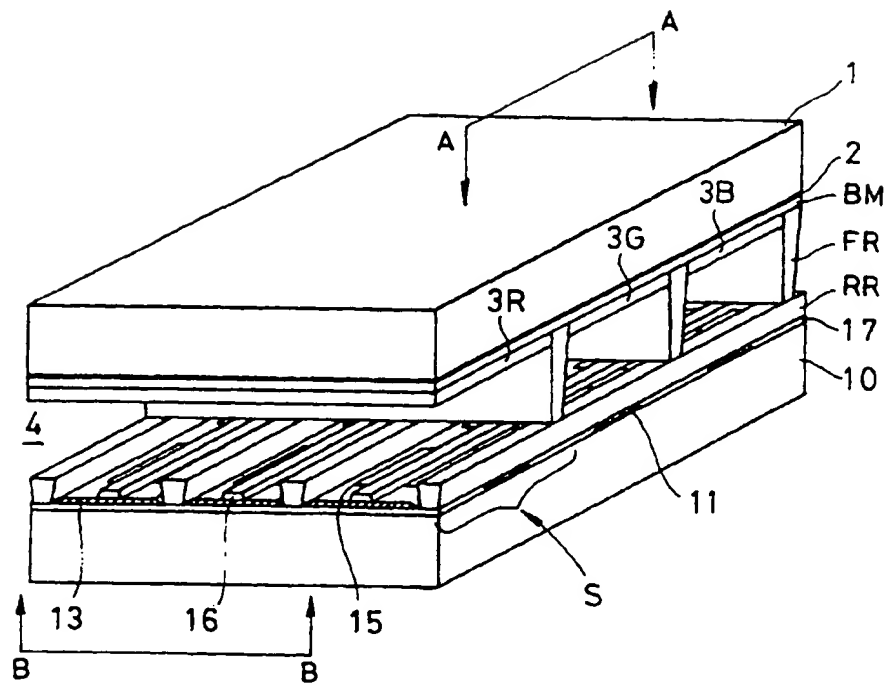
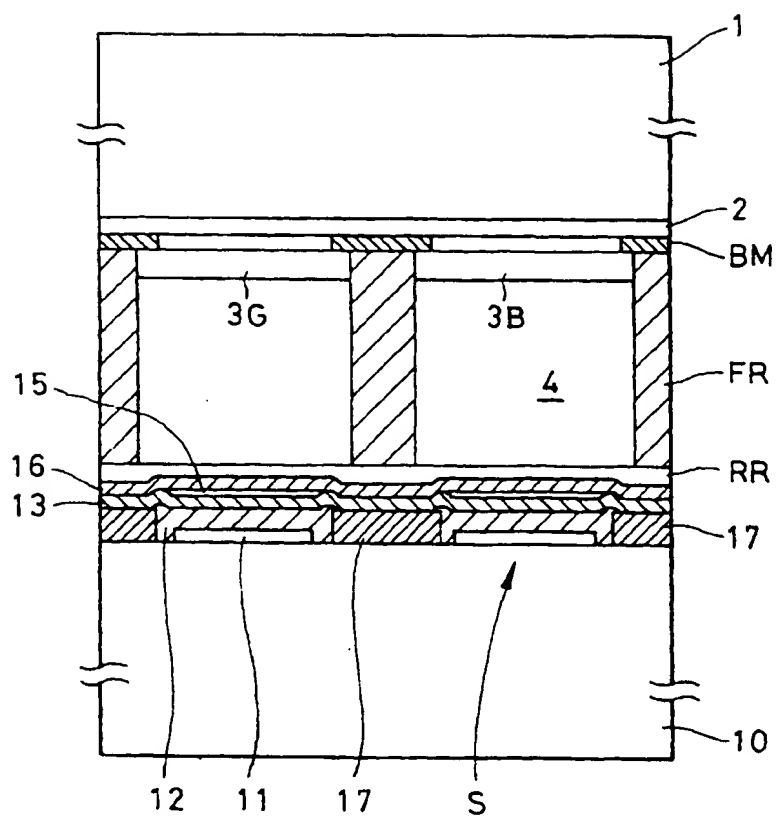


FIG.12





European Patent  
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## EUROPEAN SEARCH REPORT

Application Number  
EP 99 30 6266

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